

FIG. 1 is a block diagram of a system 10. The system 10 includes a plurality of processing units 51, 52, 53, ..., m, which are connected to an optical switch 40. The optical switch 40 is connected to a data input/output bus 60. The optical switch 40 is also connected to a data input/output bus 40. The optical switch 40 is connected to a data input/output bus 40. The optical switch 40 is connected to a data input/output bus 40.

